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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application / Conf. No.	10/815,492 / 8518
	Filing Date	April 01, 2004
	First Named Inventor	Tarek Eldin
	Examiner Name	Saqib Javaid Siddiqui
	Art Unit	2138
	Patent No.	
Mail Stop: AF	Attorney Docket Number	X-1481 US
Express Mail Receipt No.		
Total Number of Pages in This Submission		

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Customer Number	24309 (Customer Number)	Reg. Number 36,480
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Date	November 15, 2006	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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X-1481 US
10/815,492

PATENT
Conf. No.: 8518

IN THE UNITED STATES PATENT OFFICE

Applicants: Tarek Eldin et al.

Assignee: Xilinx, Inc.

Title: Method of Automatic Fault Isolation in a Programmable Logic Device

Serial No.: 10/815,492

Filing Date: April 1, 2004

Examiner: Saqib J. Siddiqui

Art Unit: 2138

Docket No.: X-1481 US

Conf. No.: 8518

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the final Office action mailed from the Patent Office on September 27, 2006, the undersigned requests that above-referenced patent application be amended as follows, in accordance with 37 C.F.R. § 1.116(b)(1), (2), or (3).

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 8 of this paper.